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gmfsk-0.6.tar.bz2/gmfsk-0.6/src/misc/viterbi.c - Package Browser

pkg://gmfsk-0.6.tar.bz2:453520/gmfsk-0.6/src/misc/viterbi.c downloads ... viterbi *v, int traceback) { if (traceback < 0 || traceback > PATHMEM - 1) return ...
www.hilawaicher.com/p/gmfsk-0.6.tar.bz2.453520/gmfsk-0.6/src/misc/viterbi.c.html · 11k -
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Koders Code Search: ProbabilisticModel.h - C++

... if (viterbi[0 + ij] < newVal) { viterbi[0 + ij] = newVal; traceback[0 + ij] ... else { viterbi[2*k+1 + ij] = valFromIns; traceback[2*k+1 + ij] = 2*k+1; ...
www.koders.com/cpp/fidF6FE9186F0E13031B13170EEBE7C237140911B0.aspx -
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int viterbi_set_traceback(struct viterbi *v, int traceback) { if (traceback < 0 || traceback > PATHMEM - 1) return -1; v->traceback = traceback; return 0; ...
www.gnsschina.com.cn/index.php3?file=detail.php3&kdir=2582516&nowdir=2582516&id=999585&de... · 19k -
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amap-align - Google Code

float newVal = viterbi[k + i1j1] + transProb[k][0] + matchProb[c1][c2];. if (viterbi[0 + ij] < newVal) { viterbi[0 + ij] = newVal; traceback[0 + ij] = k; ...
code.google.com/p/amap-align/source/browse/trunk/align/ProbabilisticModel.h?r=27 · 148k -
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Diff of ProbabilisticModel.h r27 - amap-align - Google Code

Changes to /trunk/align/ProbabilisticModel.h. r2 vs. r27 Edit. Compare: . r2, r27. vs. r2, r27.
Format: Single-column, Side by side ...
code.google.com/p/amap-align/source/diff?r=27&format=side&path=/trunk/align/ProbabilisticModel.h · 200k -
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[med-svn] r993 - in trunk/packages/probcons/trunk: . debian

[med-svn] r993 - in trunk/packages/probcons/trunk: . debian. charles.guest at alioth.debian.org charles-guest at alioth.debian.org ...
lists.alioth.debian.org/pipermail/debian-med-commit/2007-December/000981.html · 242k -
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[med-svn] r1453 - in trunk/packages/amap-align: tags tags/2.0-3

... skip posterior calculations if we just want to do Viterbi alignments - if (! all-pairs pairwise Viterbi alignments - else if (!strcmp(argv[i]), ...
lists.alioth.debian.org/pipermail/debian-med-commit/2008-February/001453.html · 239k -
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Viterbi decoder - Wikipedia, the free encyclopedia

A viterbi decoder uses the Viterbi algorithm for decoding a bitstream that ... The results of these decisions are written to the memory of a traceback unit. ...

en.wikipedia.org/wiki/Viterbi_decoder - 36k - Cached - Similar pages - Note this

CommsDesign - Crafting a Custom Viterbi Decoder for WLAN Designs

AI, "A VLSI design for a Traceback Viterbi Decoder," IEEE Transaction on Communications, vol.40, No.3, March 1992, p.616-624. Boo, M., Arguello, F., ...

www.commsdesign.com/design_corner/showArticle.html?articleID=16504016 - 51k -

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storage for decision vectors at the traceback block in the Viterbi decoder. ... proposed architecture compared to conventional traceback Viterbi decoders. ...

ieeexplore.ieee.org/iel1/2220/10465/00491059.pdf - Similar pages - Note this

Design of a performance enhanced traceback algorithm for the ...

ance improved traceback algorithm for the traceback Viterbi decoder is proposed.

Traceback Viterbi decoder: The Viterbi decoding algorithm ...

ieeexplore.ieee.org/iel1/2220/11118/00503605.pdf?amumber=503605 -

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viterbi traceback - continuous data mode | Comp.DSP | DSPRelated.com

Re: viterbi traceback - continuous data mode - Clay - 10:18 11-05-05. Hello Porterboy, You are on the right path (sorry I couldn't resist) about the ...

www.dsprelated.com/showmessage/35046/1.php - 32k - Cached - Similar pages - Note this

best state traceback in Viterbi | Comp.DSP | DSPRelated.com

Re: best state traceback in Viterbi - Raymond Toy - 11:43 04-08-05 ... compsavvy> I am using traceback approach for viterbi decoding. ...

www.dsprelated.com/showmessage/39865/1.php - 23k - Cached - Similar pages - Note this

Viterbi Decoding with Dual Timescale Traceback Processing ...

In this paper a new approach to traceback processing in Viterbi decoders is presented. The approach reduces memory requirements as compared to previous ...

citeseer.ist.psu.edu/67765.html - 21k - Cached - Similar pages - Note this

Data error detector for digital modems using trellis coding ...

logic means having inputs from said traceback Viterbi decoder and said symbol delay ...

The conventional sixteen traceback truncated Viterbi decoder for a ...

www.freepatentsonline.com/4662464.html - 26k - Cached - Similar pages - Note this

(WO/2004/112257) VITERBI DECODER PRODUCING MULTIPLE DECODED ...

According to the disclosed embodiment, a windowed traceback Viterbi decoding can be implemented in an ASIC in an efficient manner, enabling a data ...

www.wipo.int/pctdb/en/wo.jsp?IA=WO2004112257&DISPLAY=DESC - 55k -

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Viterbi decoder and viterbi decoding method - US Patent 6041433

Collins et al., "Memory Management in Traceback Viterbi Decoders", TDA Progress Report

42-99, Nov. 15, 1989, pp. 98-104. Feygin et al., "Survivor Sequence ...

www.patentstorm.us/patents/6041433.html - 16k - [Cached](#) - [Similar pages](#) - [Note this](#)

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[best state traceback in Viterbi | Comp.DSP | DSPRelated.com](#)

Re: best state traceback in Viterbi - 02:55 04-08-05. It is common to return to the zero state after every D bits at the transmitter for precisely this ...

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[Viterbi Decoder \(Reference\)](#)

The Traceback depth parameter, D, influences the decoding delay. The decoding delay is the number of zero symbols that precede the first decoded symbol in ...

[www.nbs.ntu.edu.sg/userguide/MatLab/MatLab6/toolbox/commblks/ref/viterbjdecoder.html](#) - 12k - [Cached](#) - [Similar pages](#) - [Note this](#)

[Distributed ring control circuits for Viterbi traceback - Patent ...](#)

A Viterbi decoder comprising: a traceback memory having: a memory chip having using a single binary bit (i.e., either a logic one or a logic zero). ...

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[Viterbi decoder - Wikipedia, the free encyclopedia](#)

The results of these decisions are written to the memory of a traceback unit. ... These Return-to-Zero signals are translated into a Non-Return-to-Zero form ...

[en.wikipedia.org/wiki/Viterbi_decoder](#) - 36k - [Cached](#) - [Similar pages](#) - [Note this](#)

[Tutorial on Convolutional Coding with Viterbi Decoding ...](#)

Research has shown that a traceback depth of K x 5 is sufficient for Viterbi decoding with the type of codes we have been discussing. ...

[home.netcom.com/~chip.f/viterbi/algthms2.html](#) - 51k - [Cached](#) - [Similar pages](#) - [Note this](#)

[CommsDesign - Crafting a Custom Viterbi Decoder for WLAN Designs](#)

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[Design of a Viterbi decoder with low power using minimum ...](#)

the traceback unit in the Viterbi decoder to below one fifth of that of a conventional zero. Finally, the data outputs are obtained by combining the ...

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[Distributed ring control circuits for Viterbi traceback - US ...](#)

The Viterbi decoder 115 includes a traceback memory, ... Flip flop 128 will also be reset at this time (to a logic zero) so that there is only one logic one ...

[www.patentstorm.us/patents/7275204-description.html](#) - 41k - [Cached](#) - [Similar pages](#) - [Note this](#)

[\(WO/2003/069866\) TRACEBACK OPERATION IN VITERBI DECODING FOR RATE ...](#)

Traceback Operation in Viterbi Decoding for Rate-k/n Convolutional Codes ... the trellis to the zero state, providing a known point to begin traceback. ...

[www.wipo.int/pctdb/en/wo.jsp?IA=WO2003069866&DISPLAY=DESC](#) - 36k - [Cached](#) - [Similar pages](#) - [Note this](#)

Decode convolutionally encoded data using Viterbi algorithm - Simulink

The Viterbi Decoder block decodes input symbols to produce binary output symbols. ... Sets the traceback memory to zero. Using a reset port on this block is ...

www.mathworks.com/access/helpdesk/help/toolbox/comm/blocks/ref/viterbidecoder.html - 29k - Cached - Similar pages - Note this

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Viterbi decoder with survivor bits stored to support look-ahead ...

5987637, November, 1999, Thomas, 714/795, Traceback stage for a viterbi decoder a data stream with itself to add a level of dependence on past values. ...

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which adds a level of dependence on past values. zero state, providing a known point to begin traceback. In the metric update, data is stored for each ...

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method is to wait until an entire frame of data is received before beginning traceback. In this case, tail bits are added. to force the trellis to the zero ...

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Viterbi decoder with survivor bits stored to support look-ahead ...

Traceback stage for a viterbi decoder Issued on: November 16, 1999 input bits of a data stream with itself to add a level of dependence on past values. ...

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Details and datasheet on part: CS3411TK

The CS3411 Viterbi Decoder is a high performance implementation suitable for a ... 7

Traceback length = 170 3-bit soft decision and 1-bit hard decision ...

www.digchip.com/datasheets/parts/datasheet/554/CS3411TK.php - 22k -

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Details and datasheet on part: CS3410XE

Viterbi/TCM decoder Constraint length = 7 Traceback length = 170 Supports ... Convolutional signal with itself adds a level of dependence on the past values. ...

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The Viterbi Decoder IP supports both one and two traceback schemes. ... not a multiple of the traceback length, the Zero Padding Unit automatically adds ...

www.latticesemi.com/dynamic/view_document.cfm?document_id=9015 -

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[CS3411 \(Conexant\) - Convolutional Viterbi Decoder \(Intelsat ...](#)
Viterbi, Decoder, Noise, DECODER FEATURES, Fully compliant with INTELSAT IESS-308/
309 Block mode operation Constraint length = 7 Traceback length = 170 ...
[www.himdatashet.ru/conexant/cs3411.htm](#) - 32k - [Cached](#) - [Similar pages](#) - [Note this](#)

[\(WO/1999/029079\) TRELLIS DECODING WITH MULTIPLE SYMBOL NONCOHERENT ...](#)
Conventional Viterbi Algorithm The Viterbi algorithm accepts as input a Information
about the surviving paths is stored in a trellis traceback memory ...
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 Hu, K.; Caldwell, M.D.; Lin, W.W.;
 Consumer Electronics, 1999. ICCIE, International Conference on
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23-26 May 2005 Page(s):5441 - 5444 Vol. 6
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Mahtab, Z.;
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Byonghyo Shim; Jung Chul Suh;
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13. Viterbi decoder design and performance evaluation for mobile satellite f
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Global Telecommunications Conference, 1992. Conference Record., GLOBE
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 6-9 Dec. 1992 Page(s):432 - 436 vol.1

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14. Area-efficient VLSI architecture for the traceback Viterbi decoder suppo
 Sik Kim; Sun-Young Hwang;

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 Volume 32, Issue 8, 11 April 1996 Page(s):733 - 735

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 Hwang, E.-J.; Lee, J.-H.; Kim, S.; Na, M.-S.; Hwang, S.-Y.;

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 Yeo, E.; Augsburger, S.A.; Davis, W.R.; Nikolic, B.;

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JAFJE, Steven T. / CAMERON, Kelly B. / JONES, Christoph
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patno: WO0143384
...wherein the Viterbi decoder comprises a zero trace back Vite
receiver of claim...wherein the Viterbi decoder comprises a zero
decoder. 25. The receiver of claim 21...wherein the Viterbi dec
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...wherein the Viterbi decoder comprises a zero trace back Vite
receiver of claim 15...wherein the Viterbi decoder comprises a
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...channel. Either Tail Biting or Zero Flushing convolutional code Block Viterbi Decoder IP is compatible...Supports both Tail Biting convolutional...Supports both one and two traceback schemes different...interface diagram for Block Viterbi Decoder. The diagram shows the set of past values of input data is called the...
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Berthet, Antoine / Visoz, Raphaël (France Telecom), *UNIT TRADEMARK OFFICE GRANTED PATENT*, Jan 2007
patno:US7170948
...independent gaussian complex random variables, with the same satisfying relation (2): $E\{0104\} < mtr > < mtd... \}$. In this case, the complex noise sample with zero mean value and with variance σ^2 is...
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...SPRA071A - January 2002 1 Viterbi Decoding Techniques for level of dependence on past values. A state diagram illustrating the beginning of traceback. In this case, tail bits...force the trellis to the known point to begin traceback. In the metric update, data...
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patno:US7050517
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Joe Fra, Oct 2004
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- ☐ 7. **Method and system of iterative coding/decoding of digital data** s temporal combinations, in multiple transmission and reception Berthet, Antoine / Visoz, Raphael, *UNITED STATES PATENT PRE-GRANT PUBLICATION*, Nov 2002
patno:US20020168017

...independent gaussian complex random variables, with the sar satisfying relation (2): $20 < mtable < mtr < mtd$...In this relati complex noise sample with zero mean value and with variance ; in FIG...

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- ☐ 8. **Viterbi slicer for turbo codes**
Jaffe, Steven T. / Cameron, Kelly B. / Jones, Christopher I *PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Ju
patno:US20020067779

...wherein the Viterbi decoder comprises a zero trace back Vite receiver of claim 15...wherein the Viterbi decoder comprises a ; decoder. 25. The receiver of claim 21...wherein the Viterbi dec trace back Viterbi decoder. 31. The receiver of claim 27...

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JAFJE, Steven T. / CAMERON, Kelly B. / JONES, Christoph CORPORATION), *PATENT COOPERATION TREATY APPLICATIO*
patno:WO0143384

...wherein the Viterbi decoder comprises a zero trace back Vite receiver of claim...wherein the Viterbi decoder comprises a zero decoder. 25. The receiver of claim 21...wherein the Viterbi dec trace back Viterbi decoder. 31. The receiver of claim 27...

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MACKENTHUN, Ken (Alcatel USA Sourcing, L.P.), *EUROPEA* patno: EP1036453
...all the paths leading to the state, the Viterbi algorithm select the...depicts the prior art operation of a Viterbi decoding algorithm phase...and $w(n)$ is an independent sample of zero mean white noise...2). The mathematics of a conventional Viterbi decoding algorithm convolutional...
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- ☐ 12. Detection for digital communication receivers
Raghavan, Sreen A. (National Semiconductor Corporation) *PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Mar 2000 patno: US6038269
...Generator, an Add-Compare-Select (ACS) unit, Traceback Circuit (LIFO...and $d_{sub,k}(-1)$, are stored in the traceback circuit for The...memory allocated for state +1 in the traceback circuit, $d_{sub,k}$...
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... $w_{sub,n}$ is an independent sample of zero mean white complex noise...The mathematics of a conventional Viterbi decoding algorithm for convolutional...representation of the operation of non-coherent received symbol stream is accepted by an input port 118. During path evaluator 124 identifies...surviving paths is stored in a trellis 120. The evaluation and selection...
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Viterbi Decoding Techniques in the...Traceback 4...a level of d values. A state diagram illustrating...received before beginning tail bits...force the trellis to the zero state, providing a known path. In practice, this state...
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- ☐ 15. TRELLIS DECODING WITH MULTIPLE SYMBOL NONCOHERENT DETECTION TO COMBAT FREQUENCY OFFSET
MACKENTHUN, Ken (STANFORD TELECOMMUNICATIONS, INC.) *COOPERATION TREATY APPLICATION*, Jun 1999 patno: WO9929079
...and v_{nn} is an independent sample of zero mean white complex noise...The mathematics of a conventional Viterbi decoding algorithm for

operation of non-coherent in Viterbi decoder 116. The receiver accepted by an input port 118. During Viterbi processing, a path identifies...surviving paths is stored in a trellis traceback memory and selection...

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16. [Viterbi Decoder 1P Users Guide](#) [PDF-1017K]

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October 2005 (pug04_02.0 Viterbi Decoder User's Guide isPLv (called the traceback length), the traceback is...and two past input data is called...the delayed input to the Viterbi decoder. A decoding is zero or negligible, the error...survivor sequence in the Viterbi trellis. It can be any value...

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09729443, filed 12/04/2000 and having 3 RCE-type filings therein

Claims Priority from Provisional Application 60168809, filed 12/03/1999

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Application Number: 09/729443

Assignments

Filing or 371(c) Date: 12/04/2000 eDan

Effective Date: 12/04/2000

Application Received: 12/05/2000

Pat. Num./Pub. Num: /20020067779

Issue Date: 00/00/0000

Date of Abandonment: 00/00/0000

Attorney Docket Number: BP1235

Status: 71 /RESPONSE TO NON-FINAL OFFICE ACTION ENTERED
AND FORWARDED TO EXAMINER

Confirmation Number: 6265

Title of Invention: SYNCHRONIZATION MODULE USING A VITERBI SLICER FOR A
TURBO DECODER

Examiner Number: 80488 / TORRES, JUAN

Group Art Unit: 2611

IFW Madras

Class/Subclass: 375/341.000

Lost Case: NO

Interference Number:

Unmatched Petition: NO

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CAMERON, KELLY B.	IRVINE	CALIFORNIA
JONES, CHRISTOPHER R.	LOS ANGELES	CALIFORNIA

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09729442	6693566	150	12/04/2000	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN
09739349	7106388	150	12/15/2000	DIGITAL IF DEMODULATOR FOR VIDEO APPLICATIONS	JAFFE, STEVEN
10045283	7254167	150	10/18/2001	CONSTELLATION-MULTIPLEXED TRANSMITTER AND RECEIVER	JAFFE, STEVEN
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10184766	7139332	150	06/28/2002	QUADRATURE RECEIVER SAMPLING ARCHITECTURE	JAFFE, STEVEN
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Inventor Name Search Result

Your Search was:

Last Name = JAFFE

First Name = STEVEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10985681	Not Issued	161	11/10/2004	Waveguide based light source	JAFFE, STEVEN M.
11805185	Not Issued	30	05/21/2007	Bioanalytical instrumentation using a light source subsystem	JAFFE, STEVEN M.
60161248	Not Issued	159	10/25/1999	RESONANT MICROCAVITY DISPLAY UTILIZING MIRRORS EXHIBITING ANOMALOUS PHASE DISPERSION	JAFFE, STEVEN M.
60204645	Not Issued	159	05/17/2000	Waveguide based light source	JAFFE, STEVEN M.
60385189	Not Issued	159	05/30/2002	System and method for RMP printing	JAFFE, STEVEN M.
60774503	Not Issued	159	02/16/2006	Microtiter plate photo reactor, thermo reactor and reader: quantitative-polymerase chain reaction illumination system	JAFFE, STEVEN M.
60774907	Not Issued	159	02/16/2006	High thru-put micro capillary sequencer: capillary electrophoresis illumination system	JAFFE, STEVEN M.
60802883	Not Issued	159	05/22/2006	Capillary electrophoresis light pipe	JAFFE, STEVEN M.
60831011	Not Issued	159	07/14/2006	Well light pipe	JAFFE, STEVEN M.
60888902	Not Issued	159	02/08/2007	CAPPILARY ELECTROPHORESIS LIGHT SOURCE SUBSYSTEM	JAFFE, STEVEN M.
08094767	5469018	250	07/20/1993	RESONANT MICROCAVITY DISPLAY	JAFFE, STEVEN M.
08516944	5616986	150	08/18/1995	RESONANT MICROCAVITY DISPLAY	JAFFE, STEVEN M.
08581622	5804919	150	01/18/1996	RESONANT MICROCAVITY	JAFFE, STEVEN

				DISPLAY	M.
08827189	5955839	250	03/26/1997	INCANDESCENT MICROCAVITY LIGHTSOURCE HAVING FILAMENT SPACED FROM REFLECTOR AT NODE OF WAVE EMITTED	JAFFE, STEVEN M.
09073711	6198211	150	05/06/1998	RESONANT MICROCAVITY DISPLAY	JAFFE, STEVEN M.
60117360	Not Issued	159	01/27/1999	HIGH CONTRAST FRONT AND REAR VIEWING SURFACES FOR PROJECTION DISPLAYS	JAFFE, STEVEN M.
09433733	6775334	150	11/03/1999	EQUALIZATION AND DECISION-DIRECTED LOOPS WITH TRELLIS DEMODULATION IN HIGH DEFINITION TV	JAFFE, STEVEN T.
09550757	Not Issued	161	04/17/2000	Decision feedback equalizer and precoder ramping circuit	JAFFE, STEVEN T.
09619125	Not Issued	161	07/18/2000	Decision feedback equalizer and precoder ramping circuit	JAFFE, STEVEN T.
09685476	6344871	150	10/10/2000	NTSC interference rejection filter	JAFFE, STEVEN T.
09692805	6411659	150	10/20/2000	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
09729443	Not Issued	71	12/04/2000	Synchronization module using a Viterbi slicer for a turbo decoder	JAFFE, STEVEN T.
09900222	6518892	150	07/06/2001	STOPPING CRITERIA FOR ITERATIVE DECODING	JAFFE, STEVEN T.
09946165	7254190	150	09/04/2001	SATELLITE RECEIVER	JAFFE, STEVEN T.
10004515	6727936	150	11/02/2001	NTSC INTERFERENCE REJECTION FILTER	JAFFE, STEVEN T.
10150186	6771714	150	05/16/2002	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
10219858	6686853	150	08/15/2002	METHOD AND APPARATUS FOR ITERATIVE DECODING	JAFFE, STEVEN T.
10448062	7239357	150	05/30/2003	DIGITAL IF DEMODULATOR WITH CARRIER RECOVERY	JAFFE, STEVEN T.
10703286	6891485	150	11/07/2003	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN T.
10751148	6982659	150	01/02/2004	METHOD AND APPARATUS	JAFFE, STEVEN T.

				FOR ITERATIVE DECODING	
10776991	7352411	150	02/12/2004	DIGITAL IF DEMODULATOR	JAFFE, STEVEN T.
10791686	Not Issued	41	03/03/2004	Television functionality on a chip	JAFFE, STEVEN T.
10822403	6828926	150	04/12/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN T.
10826491	7161613	150	04/16/2004	NTSC INTERFERENCE REJECTION FILTER	JAFFE, STEVEN T.
10828450	6897791	150	04/20/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN T.
10848907	Not Issued	83	05/19/2004	Decision feedback equalizer and precoder ramping circuit	JAFFE, STEVEN T.
10895579	7027528	150	07/21/2004	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
10895879	Not Issued	71	07/21/2004	Equalization and decision-directed loops with trellis demodulation in high definition TV	JAFFE, STEVEN T.
10916919	7257764	150	08/12/2004	FEC (FORWARD ERROR CORRECTION) DECODER WITH DYNAMIC PARAMETERS	JAFFE, STEVEN T.
10976277	6985093	150	10/28/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN T.
11031107	Not Issued	93	01/07/2005	DUAL MODE QAM/VSB RECEIVER	JAFFE, STEVEN T.
11116851	Not Issued	41	04/28/2005	Interspersed training among data	JAFFE, STEVEN T.
11480803	Not Issued	41	07/03/2006	Interspersed training among data	JAFFE, STEVEN T.
11772608	Not Issued	17	07/02/2007	SATELLITE RECEIVER	JAFFE, STEVEN T.
11823225	Not Issued	25	06/27/2007	FEC (forward error correction) decoder with dynamic parameters	JAFFE, STEVEN T.
60246425	Not Issued	159	11/06/2000	Stopping criteria for decoding of turbo code	JAFFE, STEVEN T.
07851019	Not Issued	166	03/13/1992	IMAGE COMPRESSION ENCODING AND DECODING METHOD AND APPARATUS THEREFOR	JAFFE, STEVEN T.

07983741	5398143	150	12/01/1992	DATA PLACEMENT ON TAPE FOR A DIGITAL VIDEO TAPE RECORDER SUITABLE FOR HIGH SPEED PICTURE PLAYBACK	JAFFE, STEVEN T.
07983748	Not Issued	166	12/01/1992	DATA CODING FOR A DIGITAL VIDEO TAPE RECORDER SUITABLE FOR HIGH SPEED PICTURE PLAYBACK	JAFFE, STEVEN T.
08214153	5526131	150	03/17/1994	DATA CODING FOR A DIGITAL VIDEO TAPE RECORDER SUITABLE FOR HIGH SPEED PICTURE PLAYBACK	JAFFE, STEVEN T.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
08239848	5533138	150	05/09/1994	IMAGE COMPRESSION ENCODING AND DECODING METHOD AND APPARATUS THEREFOR	JAFFE, STEVEN T.
08335211	5574565	250	10/28/1994	DATA PLACEMENT ON TAPE FOR A DIGITAL VIDEO TAPE RECORDER SUITABLE FOR HIGH SPEED PICTURE PLAYBACK	JAFFE, STEVEN T.
08406216	5588025	150	03/15/1995	SINGLE OSCILLATOR COMPRESSED DIGITAL INFORMATION RECEIVER	JAFFE, STEVEN T.
08472835	5635995	150	06/07/1995	CHROMINANCE SIGNAL PROCESSOR FOR COMBINED TV/VCR SYSTEMS	JAFFE, STEVEN T.
08541911	5619154	150	10/10/1995	NUMERICAL VOLTAGE CONTROLLED OSCILLATOR	JAFFE, STEVEN T.
08565608	5757855	150	11/29/1995	DATA DETECTION FOR PARTIAL RESPONSE CHANNELS	JAFFE, STEVEN T.
08602943	5872815	150	02/16/1996	APPARATUS FOR GENERATING TIMING SIGNALS FOR A DIGITAL TELEVISION SIGNAL RECEIVER	JAFFE, STEVEN T.
08702529	5894334	150	09/05/1996	CARRIER RECOVERY SYSTEM FOR A VESTIGIAL SIDE BAND SIGNAL	JAFFE, STEVEN T.
08704787	5805242	150	09/06/1996	CARRIER INDEPENDENT TIMING RECOVERY SYSTEM FOR A VESTIGIAL SIDE BAND MODULATED SIGNAL	JAFFE, STEVEN T.
08704788	5706057	150	09/05/1996	PHASE DETECTOR IN A	JAFFE, STEVEN T.

				CARRIER RECOVERY NETWORK FOR A VESTIGIAL SIDEBAND SIGNAL	
08704789	5835532	150	09/05/1996	BLIND EQUALIZER FOR A VESTIGIAL SIDEBAND SIGNAL	JAFFE, STEVEN T.
08721663	5799037	150	09/27/1996	RECEIVER CAPABLE OF DEMODULATING MULTIPLE DIGITAL MODULATION FORMATS	JAFFE, STEVEN T.
08721867	6005640	150	09/27/1996	MULTIPLE MODULATION FORMAT TELEVISION SIGNAL RECEIVER SYSTEM	JAFFE, STEVEN T.
09036925	6169767	150	03/09/1998	UNIVERSAL NETWORK INTERFACE MODULE	JAFFE, STEVEN T.
09303783	6219088	150	04/30/1999	NTSC INTERFERENCE REJECTION FILTER	JAFFE, STEVEN T.
09433730	6842495	150	11/03/1999	DUAL MODE QAM/VSB RECEIVER	JAFFE, STEVEN T.
09433734	6222891	150	11/03/1999	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
60040149	Not Issued	159	03/10/1997	UNIVERSAL NETWORK INTERFACE MODULE	JAFFE, STEVEN T.
60106921	Not Issued	159	11/03/1998	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
60106923	Not Issued	159	11/03/1998	EQUALIZATION AND DECISION-DIRECTED LOOPS WITH TRELLIS DEMODULATION IN HIGH DEFINITION TV	JAFFE, STEVEN T.
60106938	Not Issued	159	11/03/1998	NTSC INTERFERENCE REJECTION FILTER	JAFFE, STEVEN T.
60107037	Not Issued	159	11/03/1998	DUAL MODE QAM AND VSB RECEIVER	JAFFE, STEVEN T.
11197735	Not Issued	120	08/02/2005	HDTV chip with a single if strip for handling analog and digital reception	JAFFE, STEVEN TODD
08919577	Not Issued	161	02/20/1997	NUMERICAL VOLTAGE CONTROLLED OSCILLATOR	JAFFE, STEVEN TODD

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